

L Number	Hits	Search Text	DB	Time stamp
1	4	flip-flop with power adj clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/08/08 11:05
4	80	flip-flop with latch with clock with transistor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/08/08 11:08
5	7	flip-flop with latch with clock with (n-fet or nfet or n-mos or nmos or n-channel or n adj channel) with (p-fet or pfet or p-mos or pmos or p-channel or p adj channel)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/08/08 13:30
8	1	d-latch with combination\$2 adj logic adj circuit	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/08/08 13:31
6	20	d-latch with logic adj circuit	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/08/08 13:32
-	0	adiabatic adj charging adj register	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/05/13 13:15
-	28	adiabatic adj charg\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 12:53
-	8	(adiabatic adj charg\$3) and register and transistor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 12:54
-	187	((d adj flip-flop\$1) or (d-flip-flop\$1) or (d adj flip adj flop\$1) or (d-flip adj flop\$1) or (d adj latch\$3) or (d-latch\$3)) with transistor\$1) and register	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/05/13 13:43
-	649	((d adj flip-flop\$1) or (d-flip-flop\$1) or (d adj flip adj flop\$1) or (d-flip adj flop\$1) or (d adj latch\$3) or (d-latch\$3)) with transistor\$1)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/05/13 13:44
-	386	charg\$3 adj3 recycle	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 12:53
-	61	adiabatic adj3 charg\$3	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 12:53
-	2	(charg\$3 adj3 recycle) and (adiabatic adj3 charg\$3) and register and transistor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 13:01
-	4	(charg\$3 adj3 recycle) and (adiabatic adj3 charg\$3) and transistor	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 13:35
-	223	register with transistor with data with clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 13:38
-	110	((d adj flip-flop\$1) or (d-flip-flop\$1) or (d adj flip adj flop\$1) or (d-flip adf flop\$1) or (d adj latch\$3) or (d-latch\$3)).ti. and transistor\$1 and mosfet\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 13:58

-	464	((d adj flip-flop\$1) or (d-flip-flop\$1) or (d adj flip adj flop\$1) or (d-flip adf flop\$1) or (d adj latch\$3) or (d-latch\$3)).clm. and transistor\$1 and mosfet\$1	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/06/24 13:59
-	1	recycle adj power adj source	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/07/31 12:18
-	1	2002-396252.NRAN.	DERWENT	2002/07/31 14:30
-	14	(power adj clock) and (switched adj capacitor)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/07/31 16:49
-	39	(power adj clock) with (flip-flop or flip adj flop or latch or register)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/07/31 17:20
-	152	("power clock" or "power-clock") with (generat\$3)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/07/31 17:19
-	72	((("power clock" or "power-clock") with (generat\$3)) and (flip-flop or flip adj flop or latch or register)	USPAT; EPO; JPO; DERWENT; IBM_TDB	2002/07/31 17:20